

# DDR3 ECC SODIMM VLP



## Product Features

- JEDEC Standard 1.35V(1.28V~1.45V) & 1.5V(1.425V~1.575V) Power supply
- JEDEC Standard 204-pin dual In-Line memory module
- 8 bit pre-fetch
- On Die Termination with ODT pin
- Bi-directional differential data strobe
- Average refresh period 7.8us at lower then TCASE 85°C, 3.9us at 85°C < TCASE ≤ 95°C
- Support ECC function
- Low profile of PCB
- Gold finger 30μ"
- RoHS compliant

## Overview:

ECC modules are designed to detect and correct single-bit errors that occur during data storage and transmission. ECC modules use Hamming Code or Triple Modular Redundancy for error detection and correction, and manage error corrections on their own, without requesting that the data source resend original data.

## Specifications:

Series	Unbuffered w/ECC
Module Type	DDR3 SODIMM VLP
Interface	DDR3
Form Factor	SODIMM VLP
Data Rate	1066 MT/s, 1333 MT/s, 1600 MT/s
Capacity	2GB, 4GB
Function	Unbuffered Memory with ECC
Pin Number	204pin
Width	72Bits
Voltage	1.35V, 1.5V
PCB Height	0.7 Inches
Operation Temperature	0°C ~ +85°C